

Application Data Sheet

Application Information

Application number::

Filing Date::

Application Type::

Regular

Subject Matter::

Utility

Title::

COMPUTER PROCESSOR ARCHITECTURE
SELECTIVELY USING FINITE-STATE-
MACHINE FOR CONTROL CODE
EXECUTION

Attorney Docket Number::

021202-001000US

Request for Early Publication::

No

Request for Non-Publication::

No

Total Drawing Sheets::

3

Small Entity?::

Yes

Petition included?::

No

Secrecy Order in Parent Appl.::

No

Applicant Information

Applicant Authority Type::

Inventor

Primary Citizenship Country::

US

Status::

Full Capacity

Given Name::

Paul

Middle Name::

L.

Family Name::

Master

Name Suffix::

City of Residence::

Sunnyvale

State or Province of Residence::

CA

Country of Residence::

US

Street of Mailing Address::

796 Dartshire Way

City of Mailing Address::

Sunnyvale

State or Province of mailing address::

CA

Country of mailing address:: US
Postal or Zip Code of mailing address:: 94087

Applicant Authority Type:: Inventor
Primary Citizenship Country:: US
Status:: Full Capacity
Given Name:: W.
Middle Name:: James
Family Name:: Scheuermann
City of Residence:: Saratoga
State or Province of Residence:: CA
Country of Residence:: US
Street of Mailing Address:: 21485 Saratoga Hills Road
City of Mailing Address:: Saratoga
State or Province of mailing address:: CA
Country of mailing address:: US
Postal or Zip Code of mailing address:: 95070

Correspondence Information

Correspondence Customer Number:: 20350

Representative Information

Representative Customer Number::

Domestic Priority Information

Application:: Continuity Type:: Parent Application:: Parent Filing Date::

Foreign Priority Information

Country:: Application number:: Filing Date::

Assignee Information

Assignee Name:: QuickSilver Technology, Inc.
Street of mailing address:: 6640 Via Del Oro, Suite 120
City of mailing address:: San Jose
State or Province of mailing address:: CA
Country of mailing address:: US
Postal or Zip Code of mailing address:: 95119